



PATENT
Attorney Docket No. 09812.0201-00000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Takashi YOKOKAWA) Group Art Unit: 2133
)
Application No.: 10/521,191) Examiner: RIZK, Samir Wadie
)
Filed: January 12, 2005)
)
For: DECODING METHOD,) Confirmation No.: 8642
DECODING APPARATUS, AND)
PROGRAM)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(c)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), Applicants bring to the attention of the Examiner the documents on the attached listing. This Information Disclosure Statement is being filed after the events recited in Section 1.97(b) but, to the undersigned's knowledge, before the mailing date of either a Final action, Quayle action, or a Notice of Allowance. Under the provisions of 37 C.F.R. § 1.97(c), this Information Disclosure Statement is accompanied by a fee of \$180.00 as specified by Section 1.17(p).

Copies of the non-patent literature documents are attached. Applicants respectfully request that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

07/18/2006 JADD01 00000021 10521191
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This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the

documents as prior art against any claims in the application and Applicants determine that the cited documents do not constitute "prior art" under United States law, Applicants reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

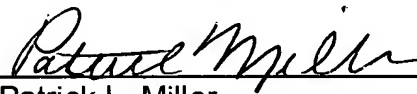
Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

Dated: July 17, 2006

By: 
Patrick L. Miller
Reg. No. 57,502

Complete if Known

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of

1

Application Number

10/521,191

Filing Date

January 12, 2005

First Named Inventor

Takashi Yokokawa

Art Unit

2133

Examiner Name

RIZK, Samir Wadie

Attorney Docket Number

09812.0201-00000

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS

Examiner Initials	Cite No. ¹	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US-			
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Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation ⁵
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation
		Robert G. Gallager, <i>Low-Density Parity Check Codes</i> (M. I. T. Press 1963).	
		David J. C. MacKay, "Good Error-Correcting Codes Based on Very Sparse Matrices," <i>IEEE Transactions on Information Theory</i> , vol. 45, no. 2, pp. 399-431, March 1999.	
		Michael G. Luby, Michael Mitzenmacher, M. Amin Shokrollahi & Daniel A. Spielman, "Analysis of Low Density Codes and Improved Design Using Irregular Graphs," <i>Proceedings of ACM Symposium on Theory of Computing</i> , pp. 249-58 (1998).	
		C. Howland & A. Blanksby, "Parallel Decoding Architectures for Low Density Parity Check Codes," <i>Symposium on Circuits and Systems</i> , part IV, pp. 742-45 (2001).	
		Engling Yeo, Payam Pakzad, Borivoje Nikolic & Venkat Anantharam, "VLSI Architectures for Iterative Decoders in Magnetic Recording Channels," <i>IEEE Transactions on Magnetics</i> , vol. 37, no. 2, pp. 748-55, March 2001.	

Examiner
SignatureDate
Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.